

## ABSTRACT

A new multiple imager array device is achieved. The device comprises, first, a bi-directional bus. An array of imagers is arranged in  $n$  rows and  $m$  columns. The  $n$  and  $m$  are positive integers. Each imager is connected to the bi-directional bus. A line of bits of any imager is accessible by a line address. Each imager has a Vmode input and an output enable input. The Vmode input and the output enable input must be enabled to allow accessing. A first multiplexer has an input and a plurality of outputs. The input is connected to a column counter. Each output is connected to the output enable input of one of the imagers. A second multiplexer has an input and a plurality of outputs. The input is connected to a row counter. Each output is connected to the Vmode input of each imager in one of the rows.